20220220.2E1E8001

Fig.1

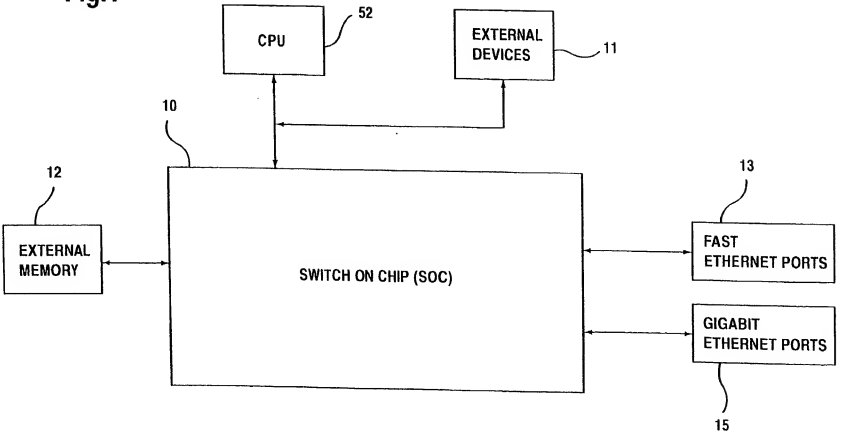
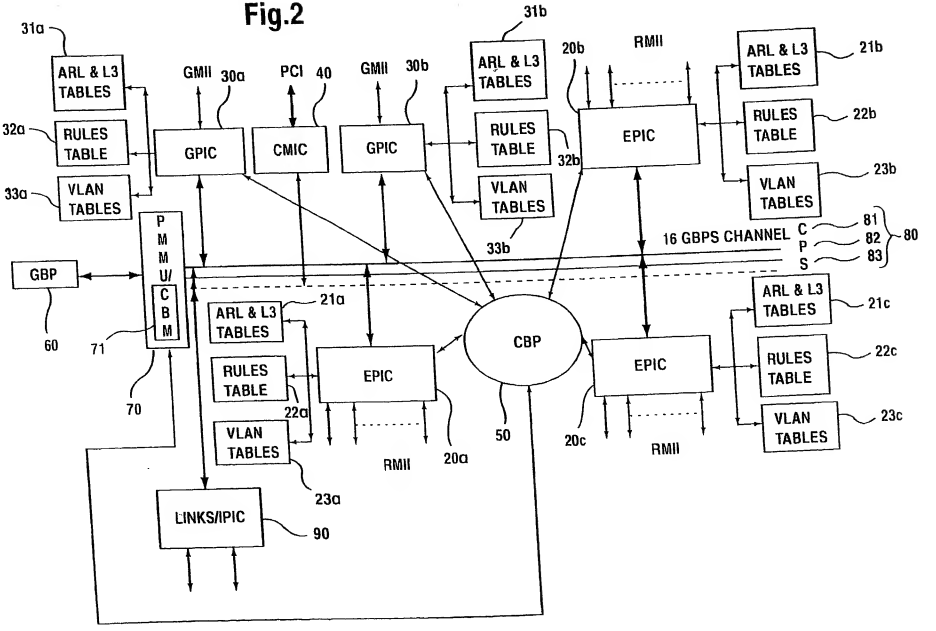
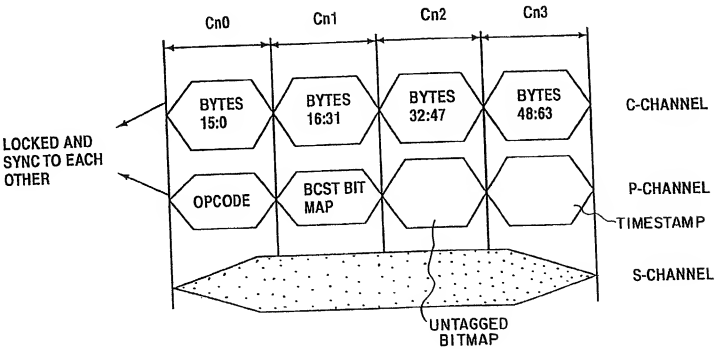


Fig.2


31a
ARL & L3 TABLES
GMII
30a
PCI
40
GMII
30b
31b
ARL & L3 TABLES
20b
RMII
ARL & L3 TABLES
21b
32a
RULES TABLE
GPIC
CMIC
GPIC
RULES TABLE
32b
EPIC
RULES TABLE
22b
33a
VLAN TABLES
VLAN TABLES
33b
VLAN TABLES
23b
16 GBPS CHANNEL
C
P
S
81
82
83
80
GBP
60
P M M U/ C B M
71
70
ARL & L3 TABLES
21a
RULES TABLE
22a
VLAN TABLES
23a
EPIC
RMII
20a
CBP
50
EPIC
20c
RMII
ARL & L3 TABLES
21c
RULES TABLE
22c
VLAN TABLES
23c
LINKS/IPIC
90

20220 2E7E800T

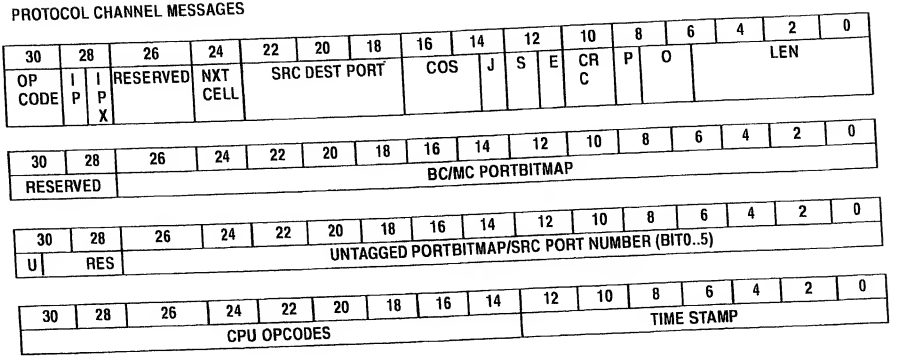
Fig.3



20220220.2E1E8001

Fig. 4

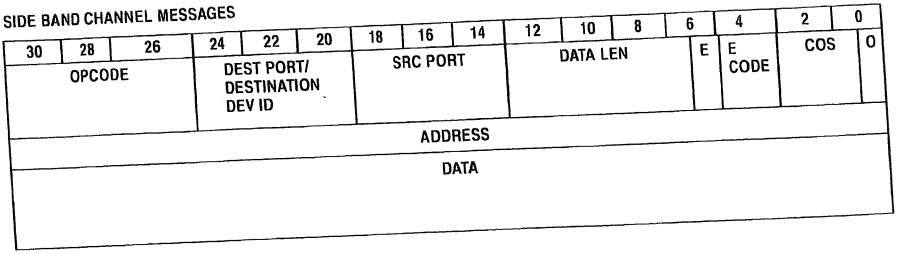
PROTOCOL CHANNEL MESSAGES



20220202 2E1E800T

Fig. 5

SIDE BAND CHANNEL MESSAGES



20220.2E1E8001

Fig. 6

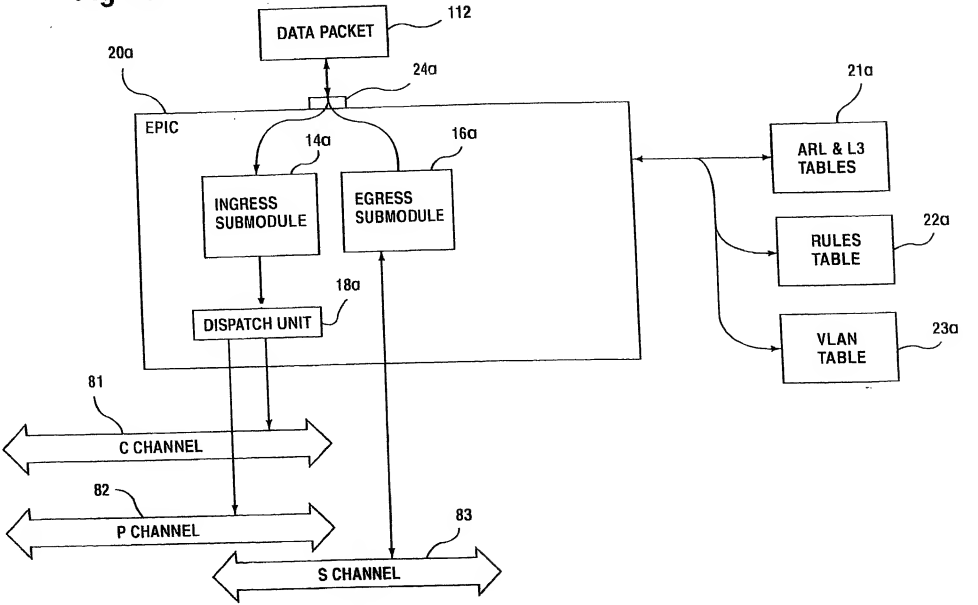


Fig. 7

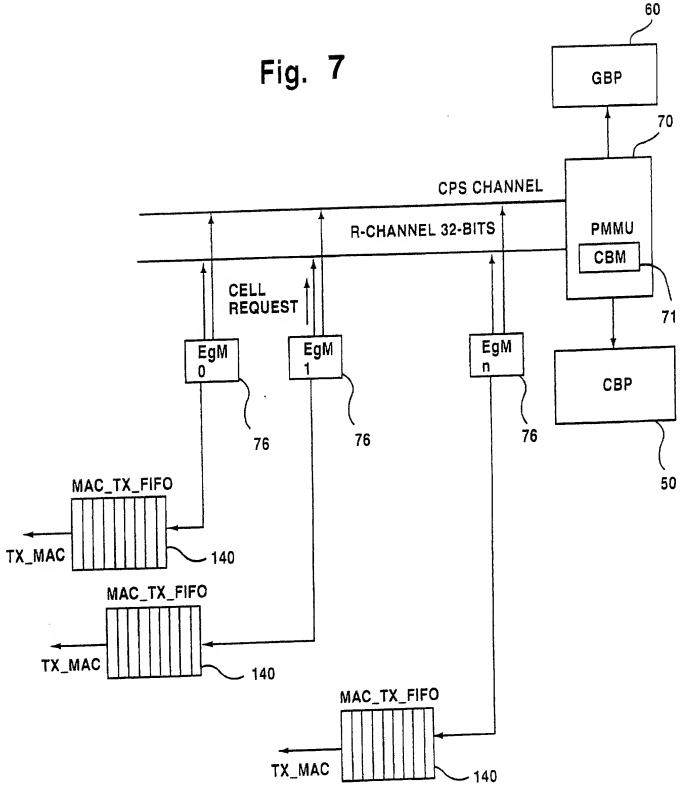
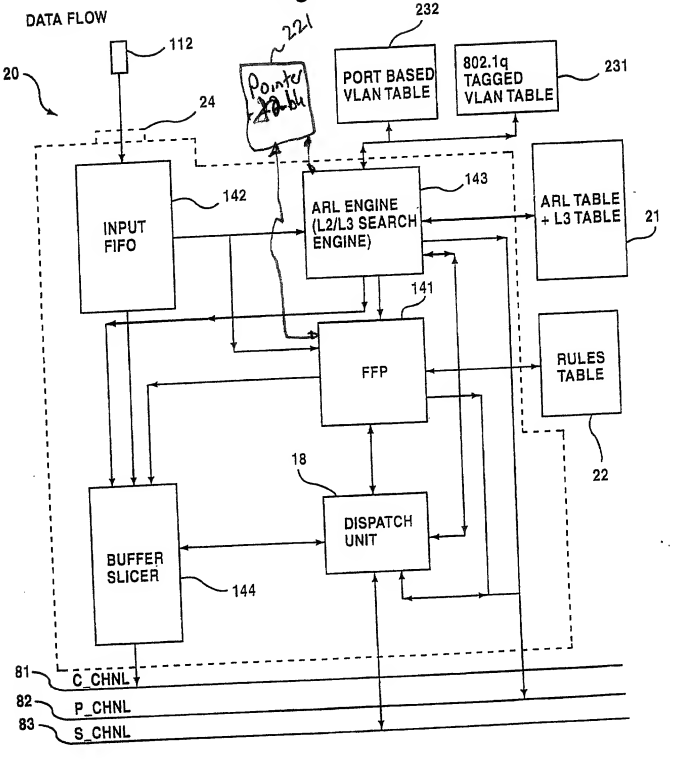

60
GBP
70
CPS CHANNEL
R-CHANNEL 32-BITS
PMMU
CBM
71
CELL
REQUEST
EgM
0
EgM
1
EgM
n
76
76
76
CBP
50
MAC_TX_FIFO
TX_MAC
140
MAC_TX_FIFO
TX_MAC
140
MAC_TX_FIFO
TX_MAC
140

Fig. 8



20070220.2213004

20/2220 2ETE8001

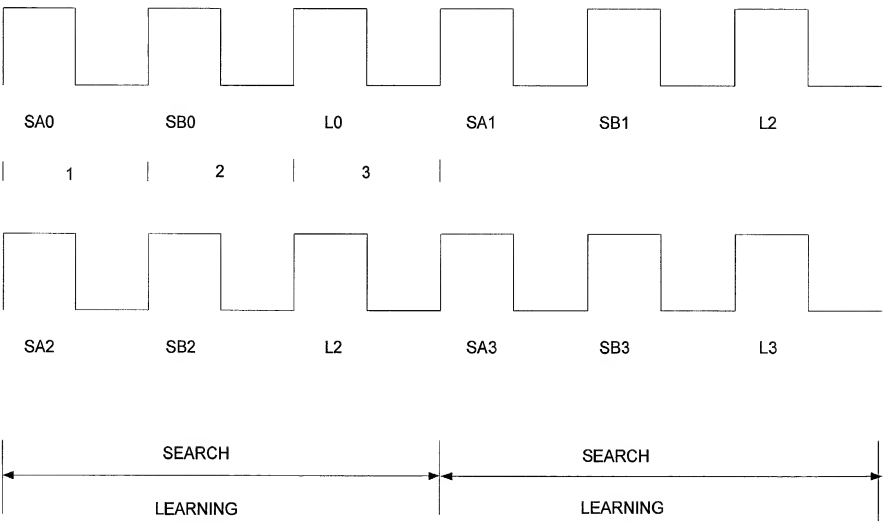


FIG 9

20220203134004

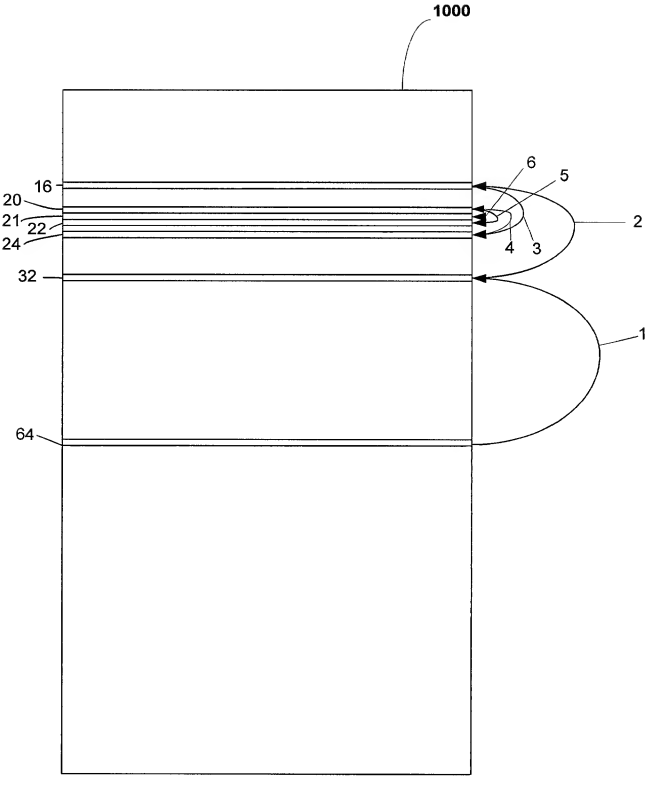


Figure 10

20220227160001

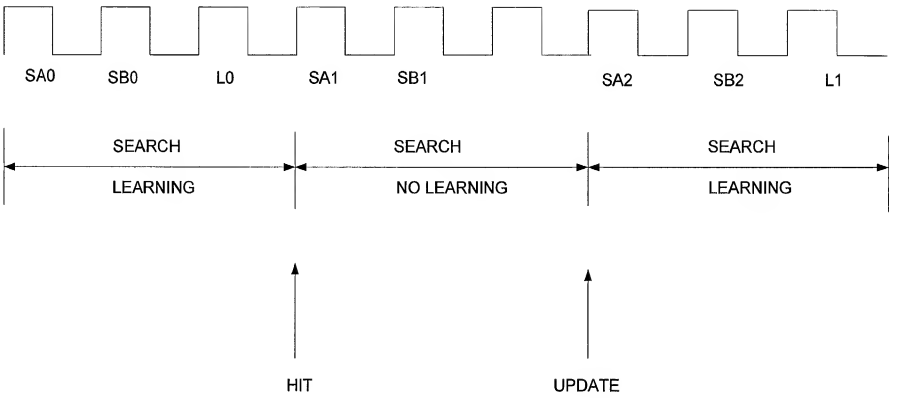


FIG 11

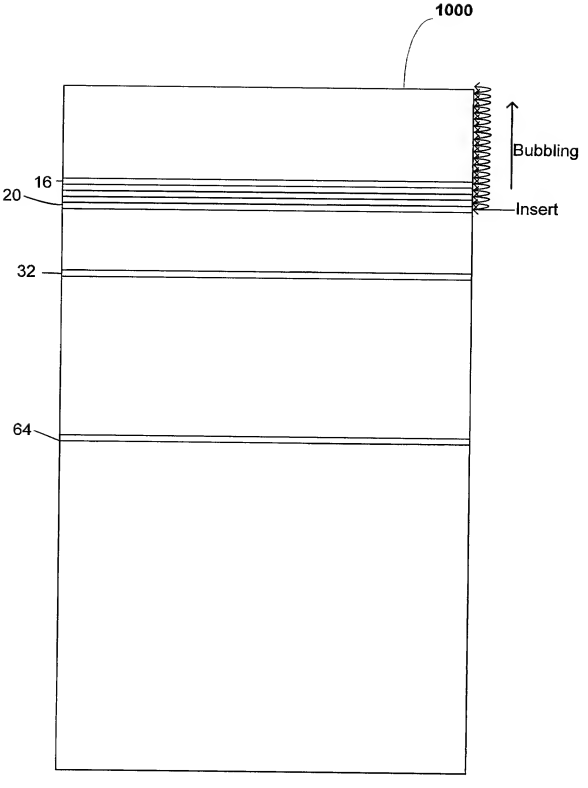


Figure 12

Step	1	3	4	5	6	7	8	9	2
Index	256	320	352	368	376	380	382	383	384
Key	1000	1300	1400	1500	1600	1700	1800	1900	2000

FIG. 13

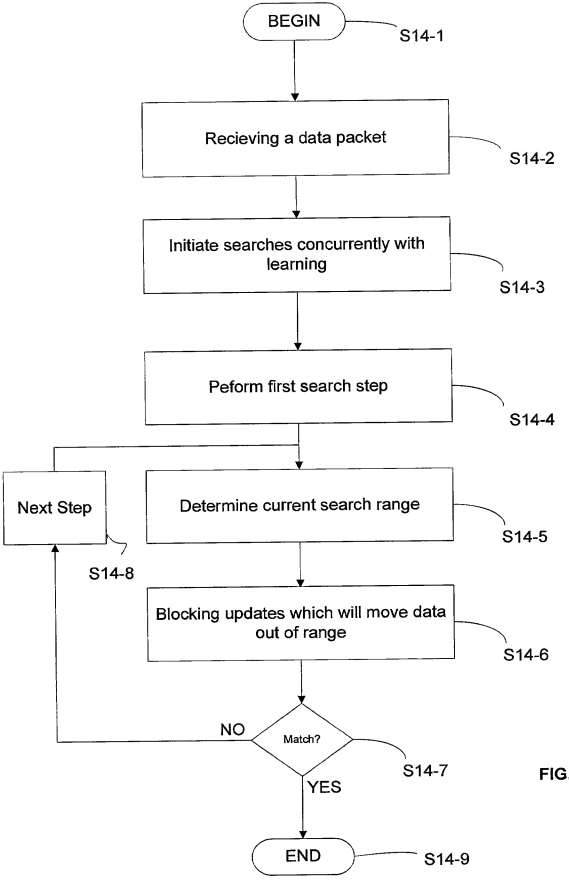

BEGIN
S14-1
Recieving a data packet
S14-2
Initiate searches concurrently with learning
S14-3
Peform first search step
S14-4
Next Step
S14-8
Determine current search range
S14-5
Blocking updates which will move data out of range
S14-6
NO
Match?
S14-7
YES
END
S14-9

FIG. 14